Appl. No. 10/710,522 Reply to Office action of December 04, 2007

Amendments to the Claims:

The listing of claims will replace all prior versions and listings of claims in the application:

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Listing of Claims:

- Claim I (currently amended): [[An]] A post-etch wet cleaning process extrusion-free wet eleaning process, comprising:
 - providing a wet cleaning tool;
- preparing a wafer having a main surface comprising at least one copper wire

 line, exposed copper feature and a dielectric film formed on the copper

 metal feature and at least one opening formed in the dielectric film,

 wherein at least a portion of the copper wire line is exposed through the opening;
- 15 transferring said wafer into said wet cleaning tool in a light inhibited manner;
 and
 - cleaning said main surface of said wafer by contacting a cleaning solution in said light inhibited manner.
- Claim 2 (currently amended): The <u>post-etch wet cleaning process extrusion-free wet-</u>
 cleaning process according to claim 1 wherein said wafer is a semiconductor wafer.
 - Claim 3 (currently amended): The <u>post-etch wet cleaning process</u> extrusion-free weteleaning process according to claim 1 wherein said exposed copper feature is damascened into said dielectric film.
- Claim 4 (currently amended): The post-etch wet cleaning process extrusion-free weteleaning process according to claim 1 wherein during said cleaning step of said main surface of said wafer in said light inhibited manner, said wafer is not exposed to light.

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- Claim 5 (currently amended): The <u>post-etch wet cleaning process</u> extrusion-free wet eleaning process according to claim 1 wherein said wet cleaning tool comprises a succession of sinks containing said cleaning solution.
- Claim 6 (currently amended): The <u>post-etch wet cleaning process</u> extrusion-free wet-5 <u>oleaning process</u> according to claim 1 wherein said wet cleaning tool is a single-wafer cleaning tool.

Claims 7 -- 11 (canceled)